FIG. 1

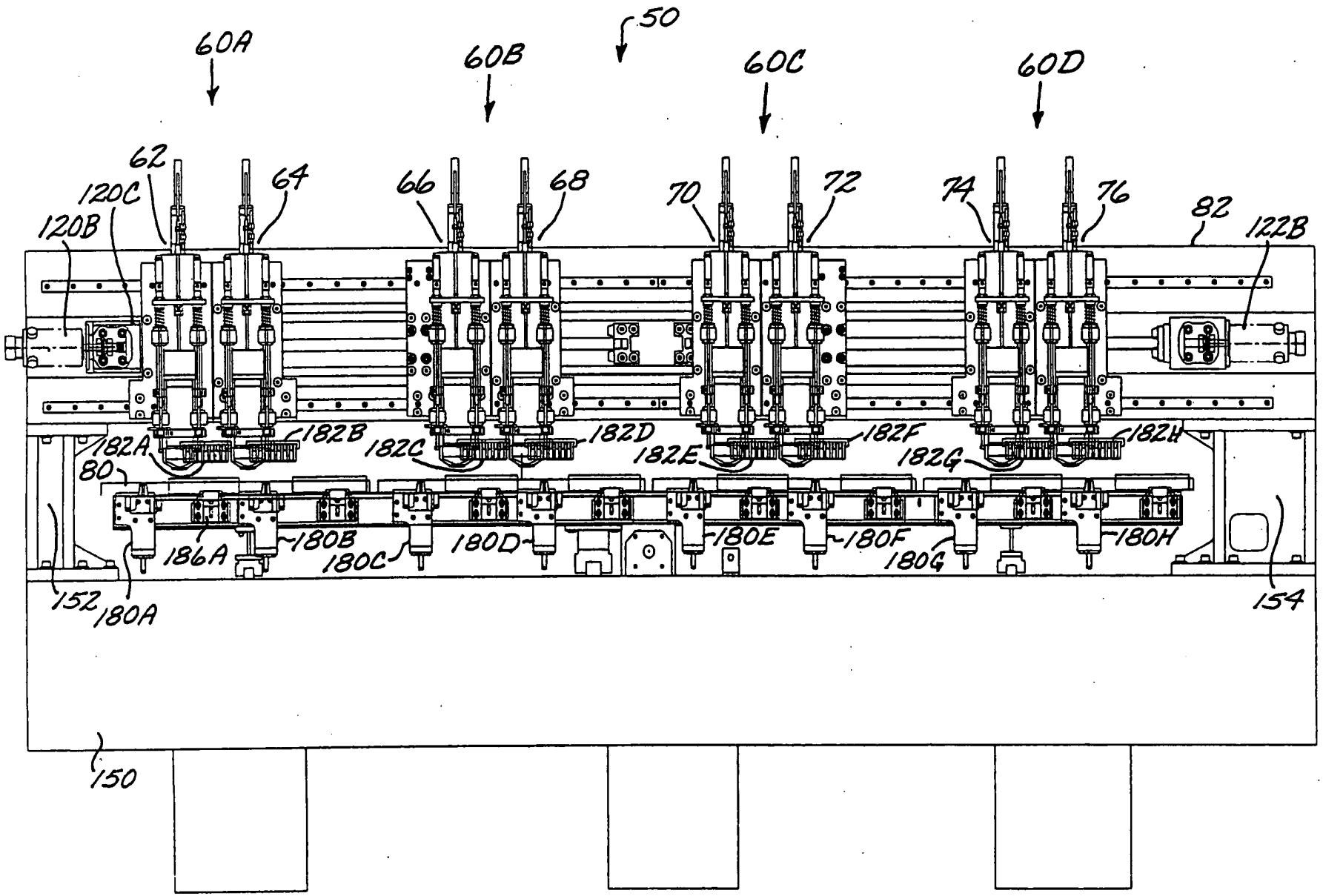
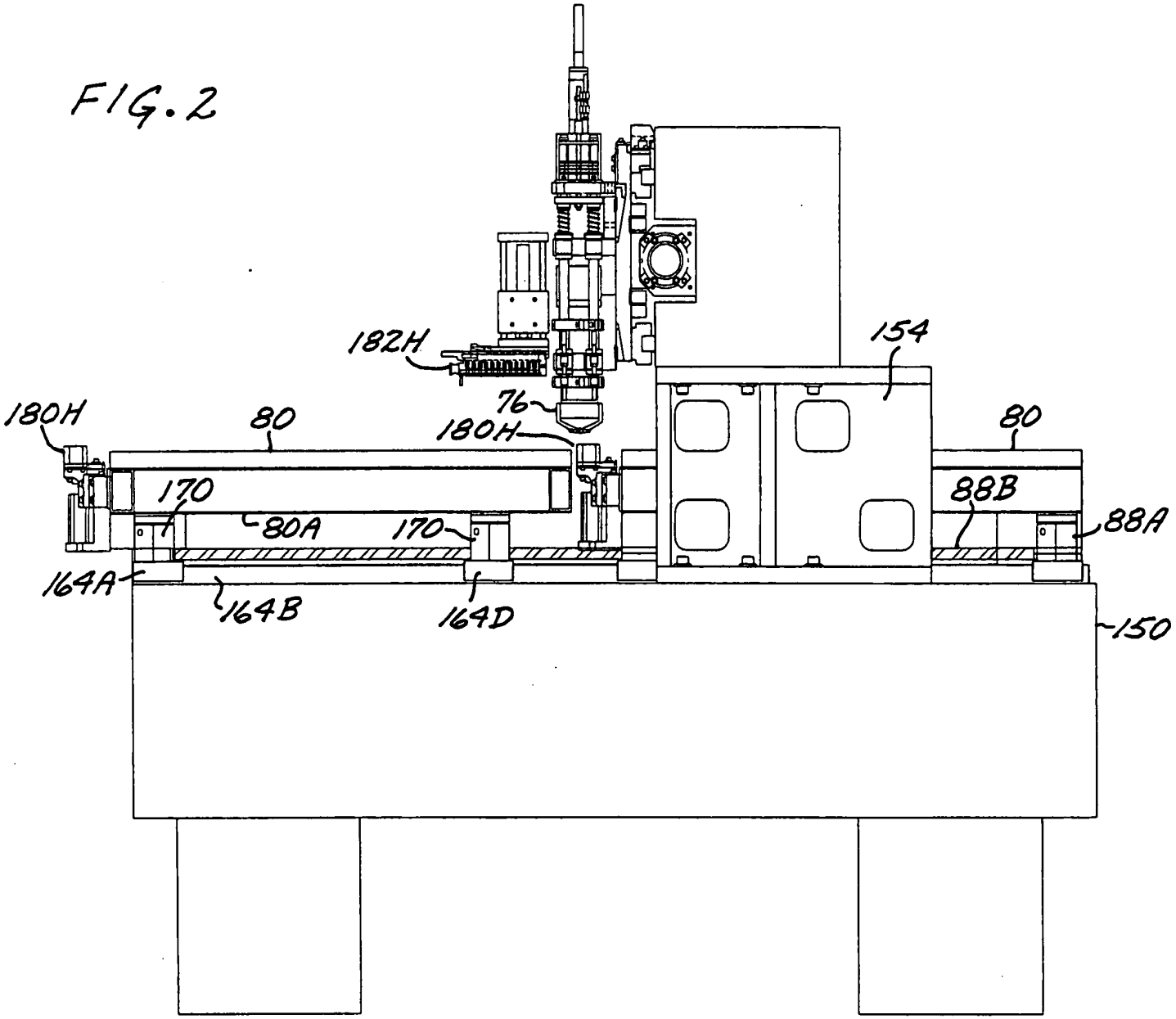

50
60A
60B
60C
60D
62
64
66
68
70
72
74
76
82
120B
120C
122B
182A
182B
182C
182D
182E
182F
182G
182H
80
186A
180A
180B
180C
180D
180E
180F
180G
180H
152
154
150

FIG. 2


182H
154
76
180H
80
180H
80
170
88B
80A
170
88A
164A
164B
164D
150

T04027 "S9T50001

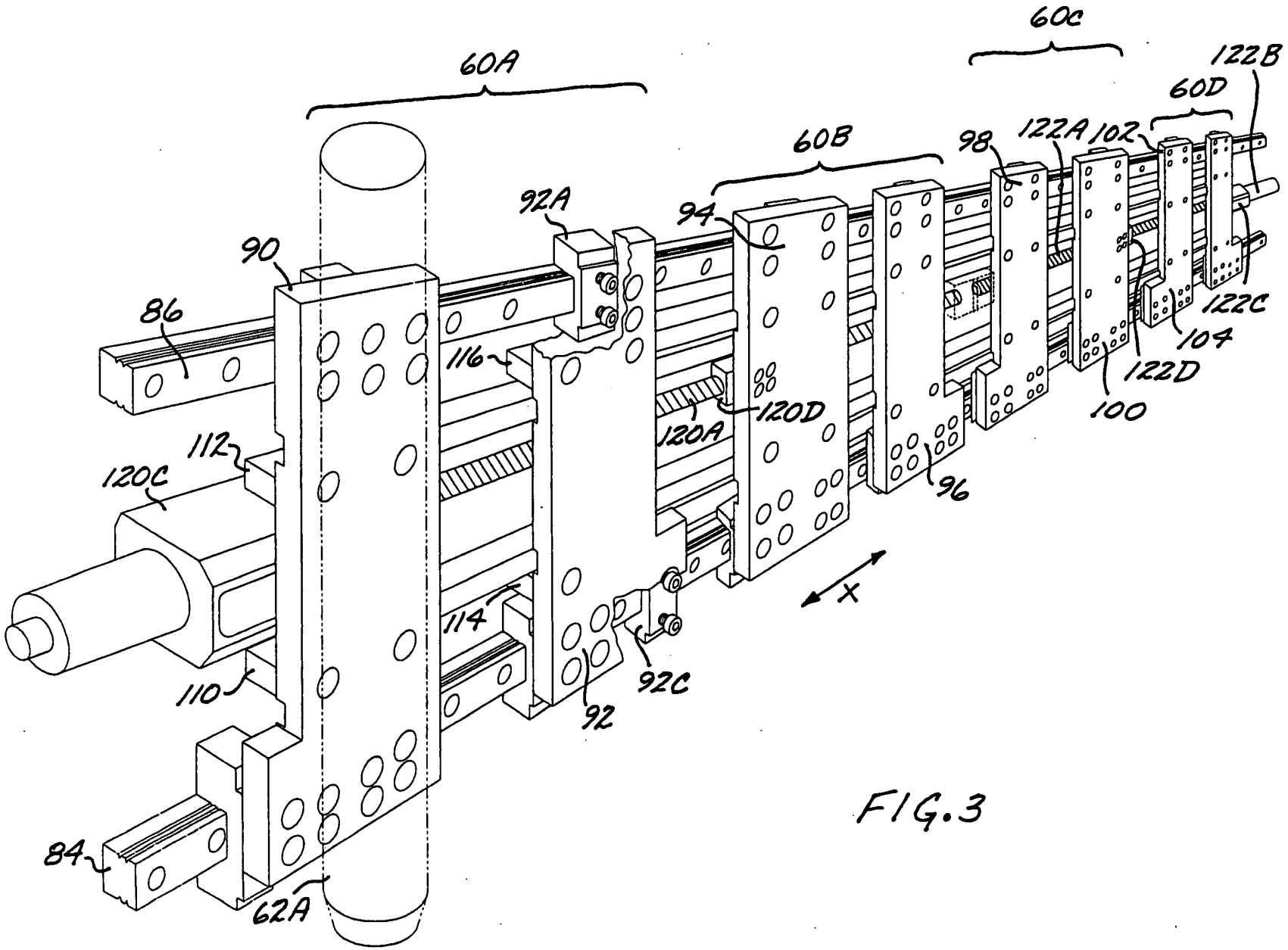
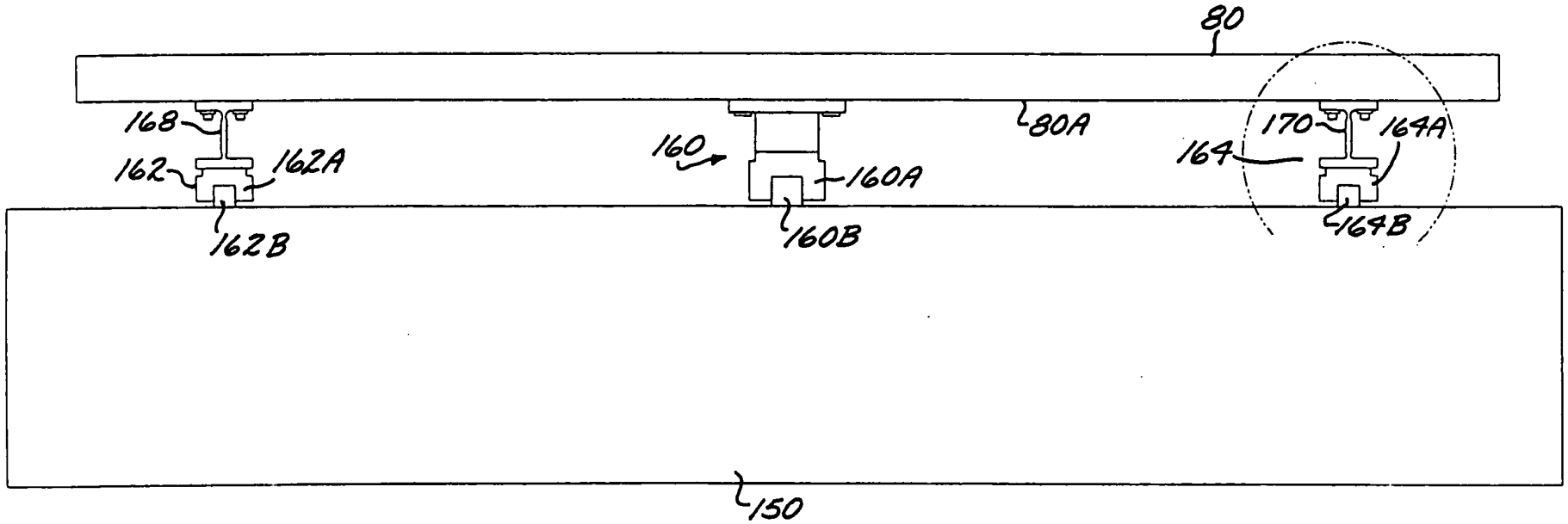


FIG. 3

FIG.4



PLOT SET 001

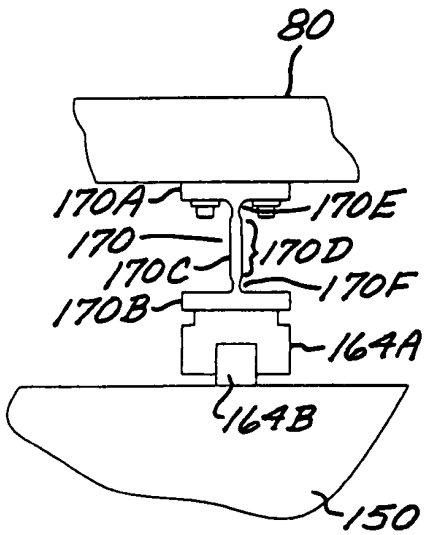


FIG.5

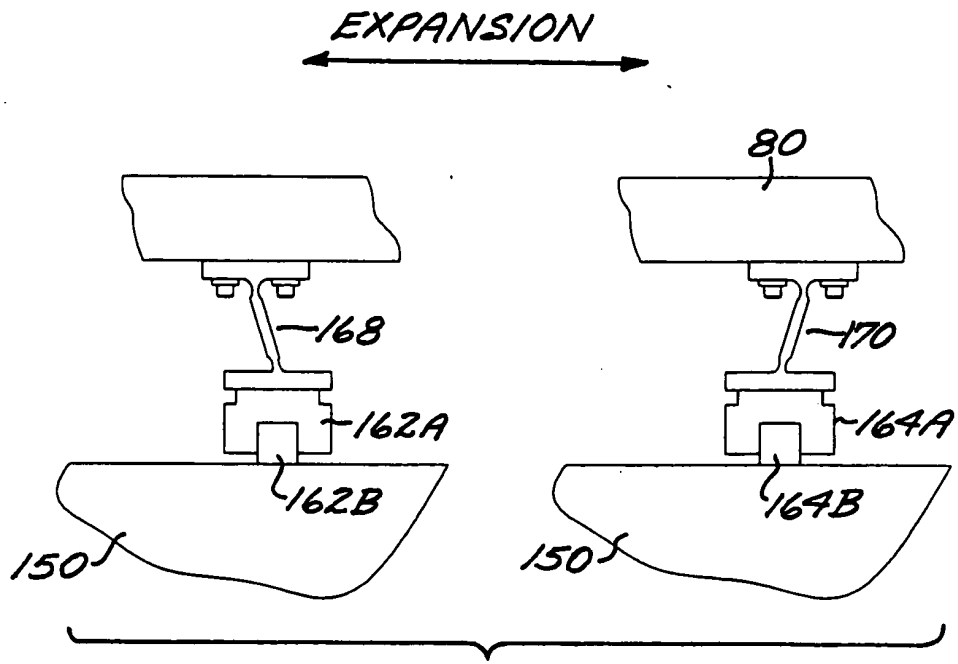


FIG.6

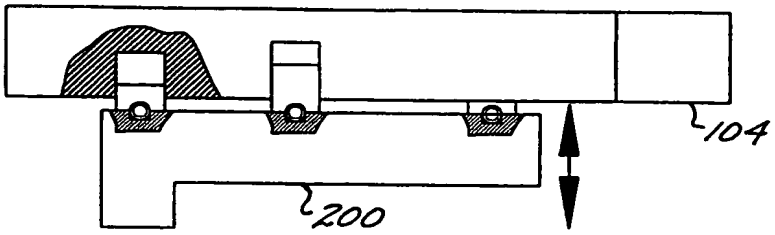


FIG.9

FIG.8

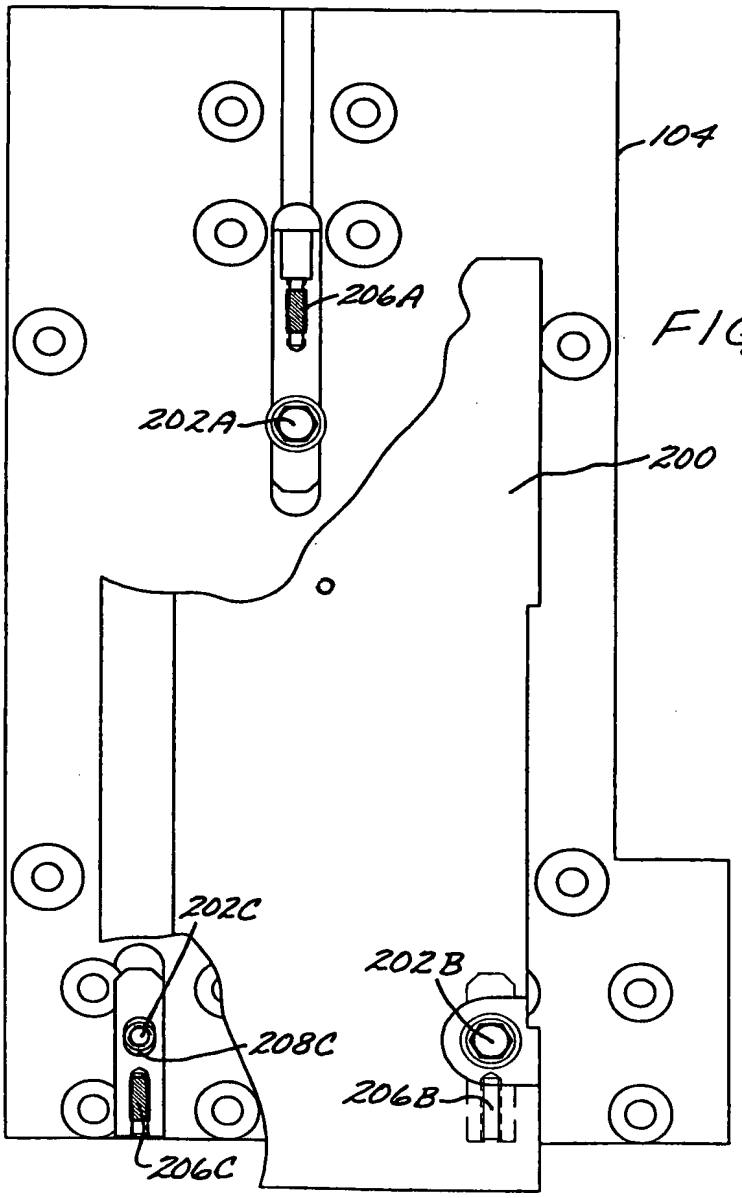
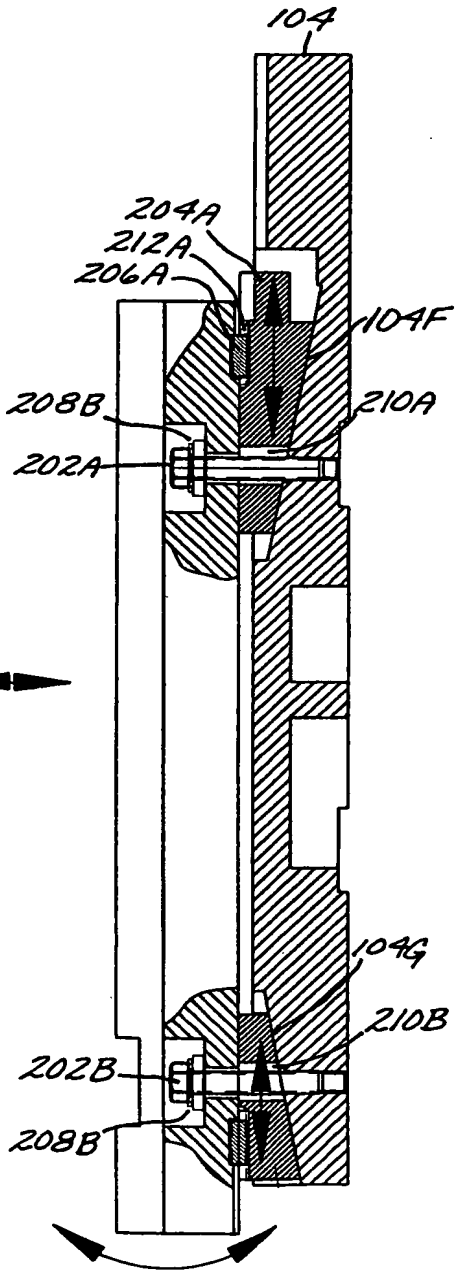


FIG.7



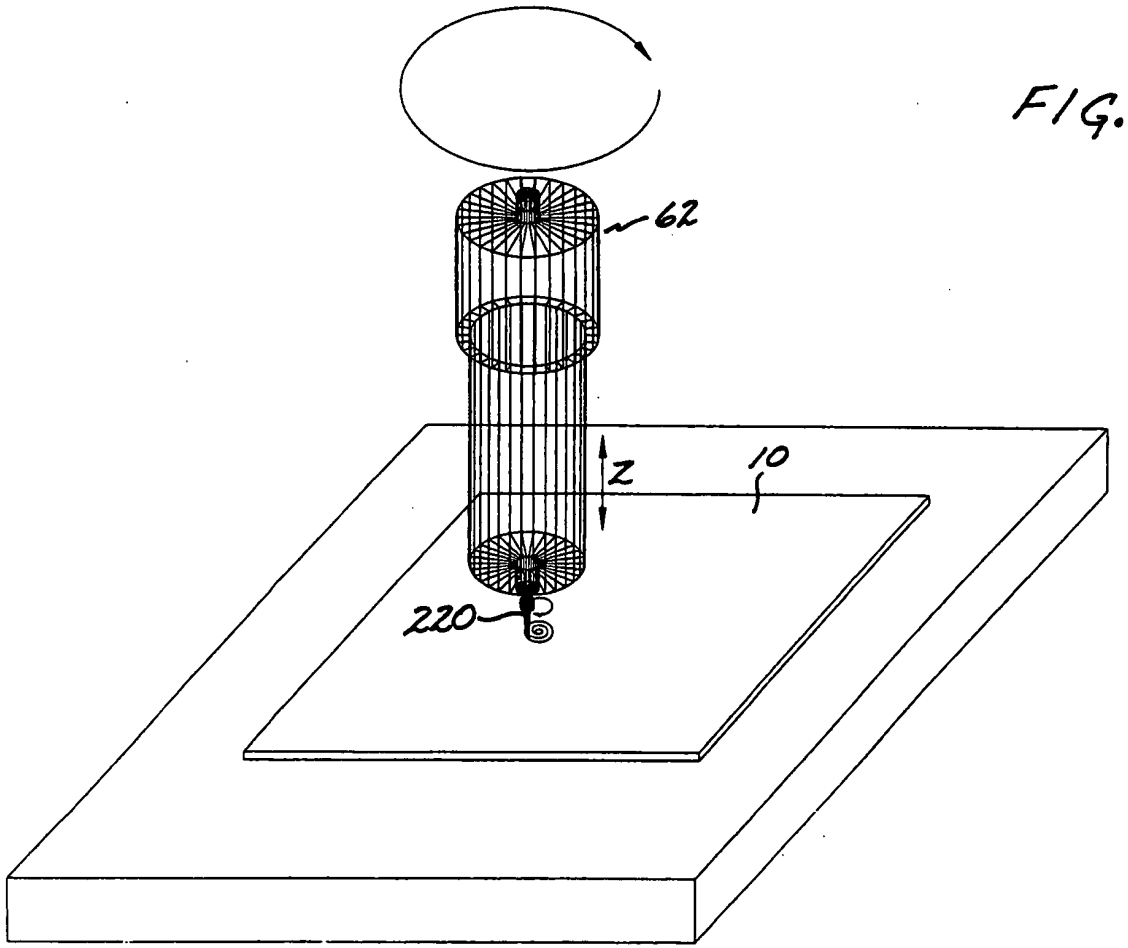
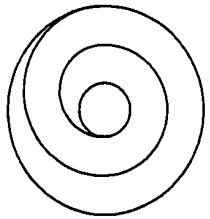


FIG.10



ROUTER PATH THROUGH THE MATERIAL TO EVACUATE HOLE VOLUME IN CHIP FORM.

T3L23T-S0T50B0F

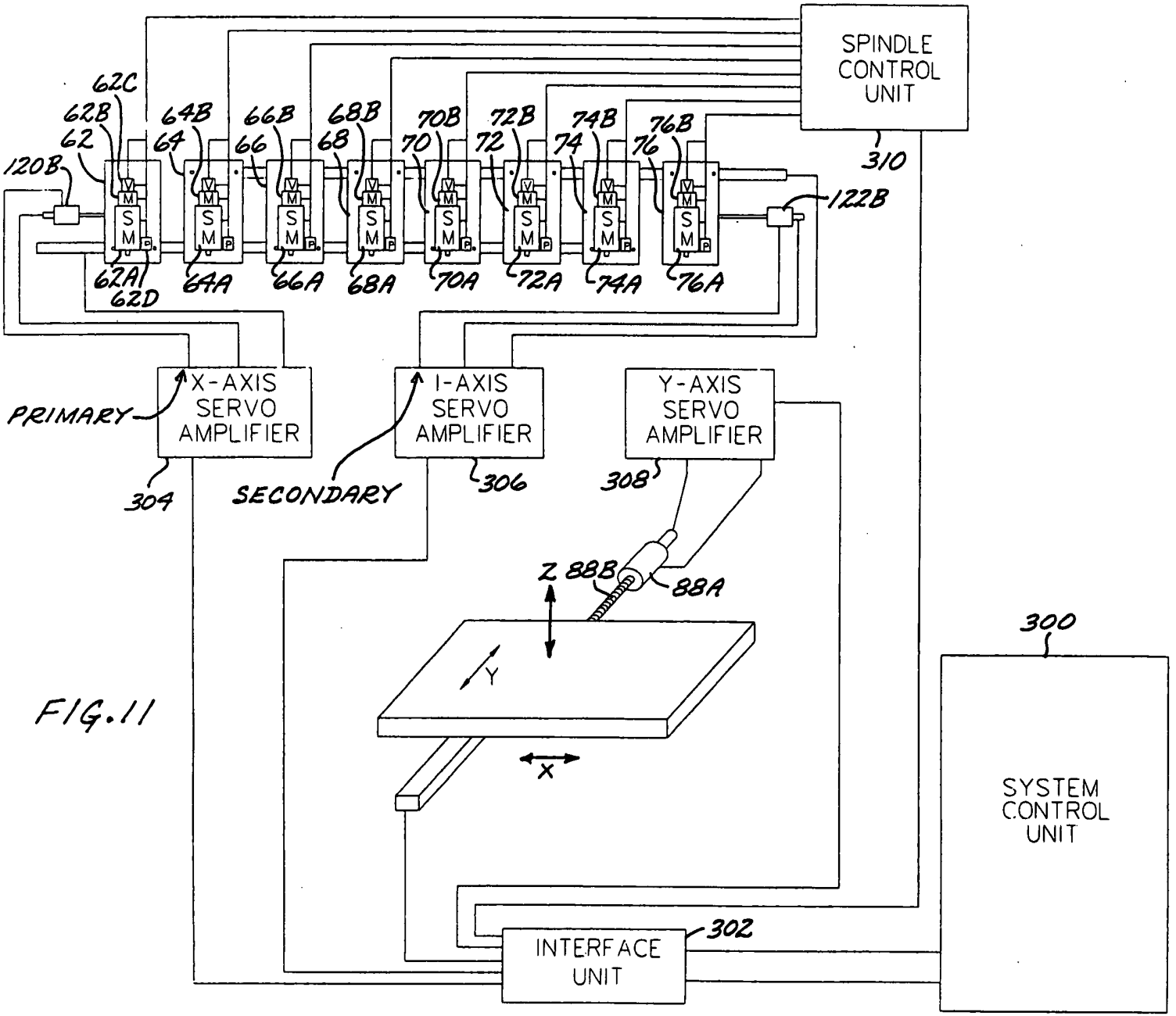
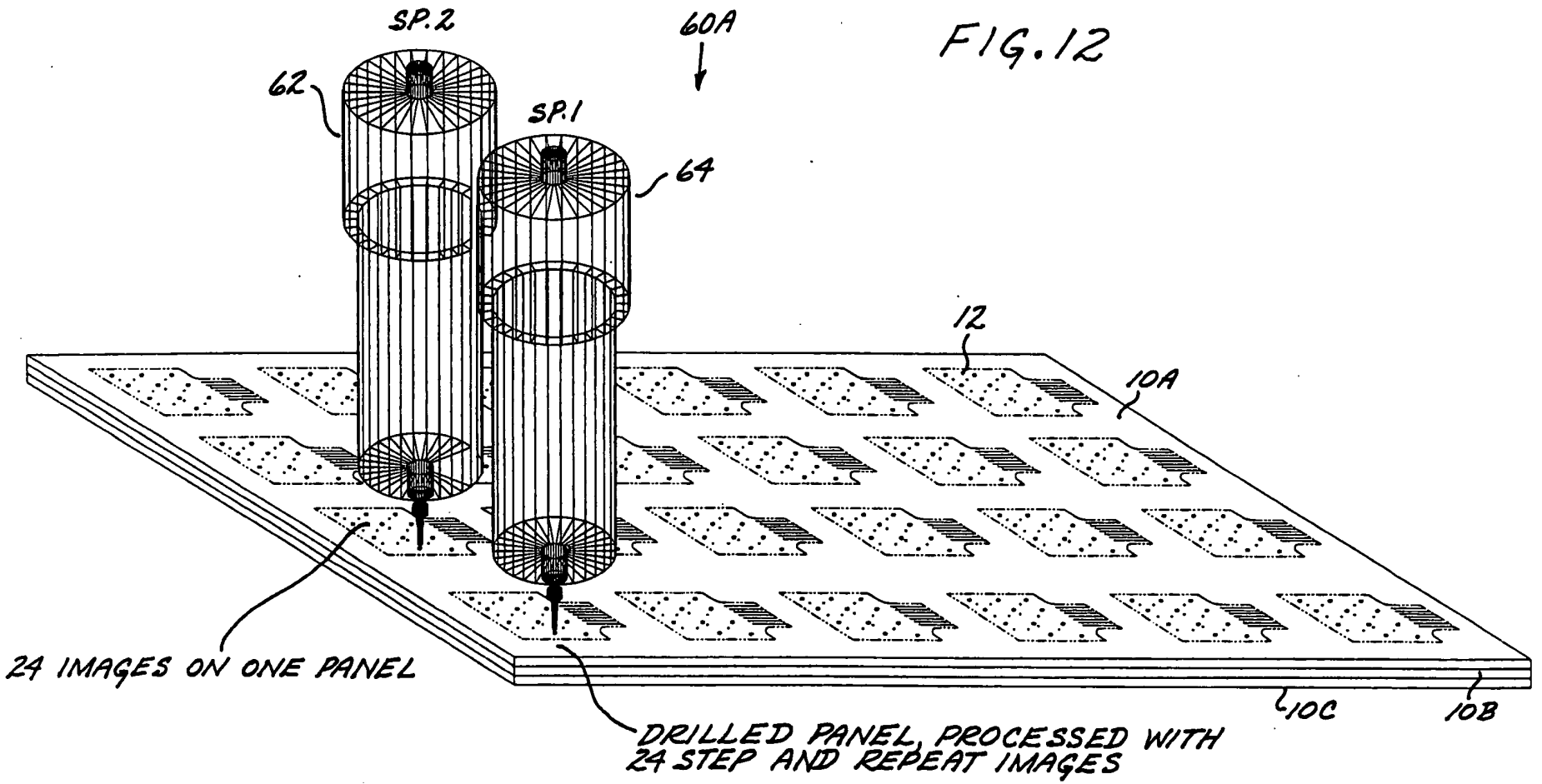


FIG. 11

FIG. 12


SP.2
60A
62
SP.1
64
12
10A
24 IMAGES ON ONE PANEL
10C
10B
DRILLED PANEL, PROCESSED WITH
24 STEP AND REPEAT IMAGES

T04027 S8T50007

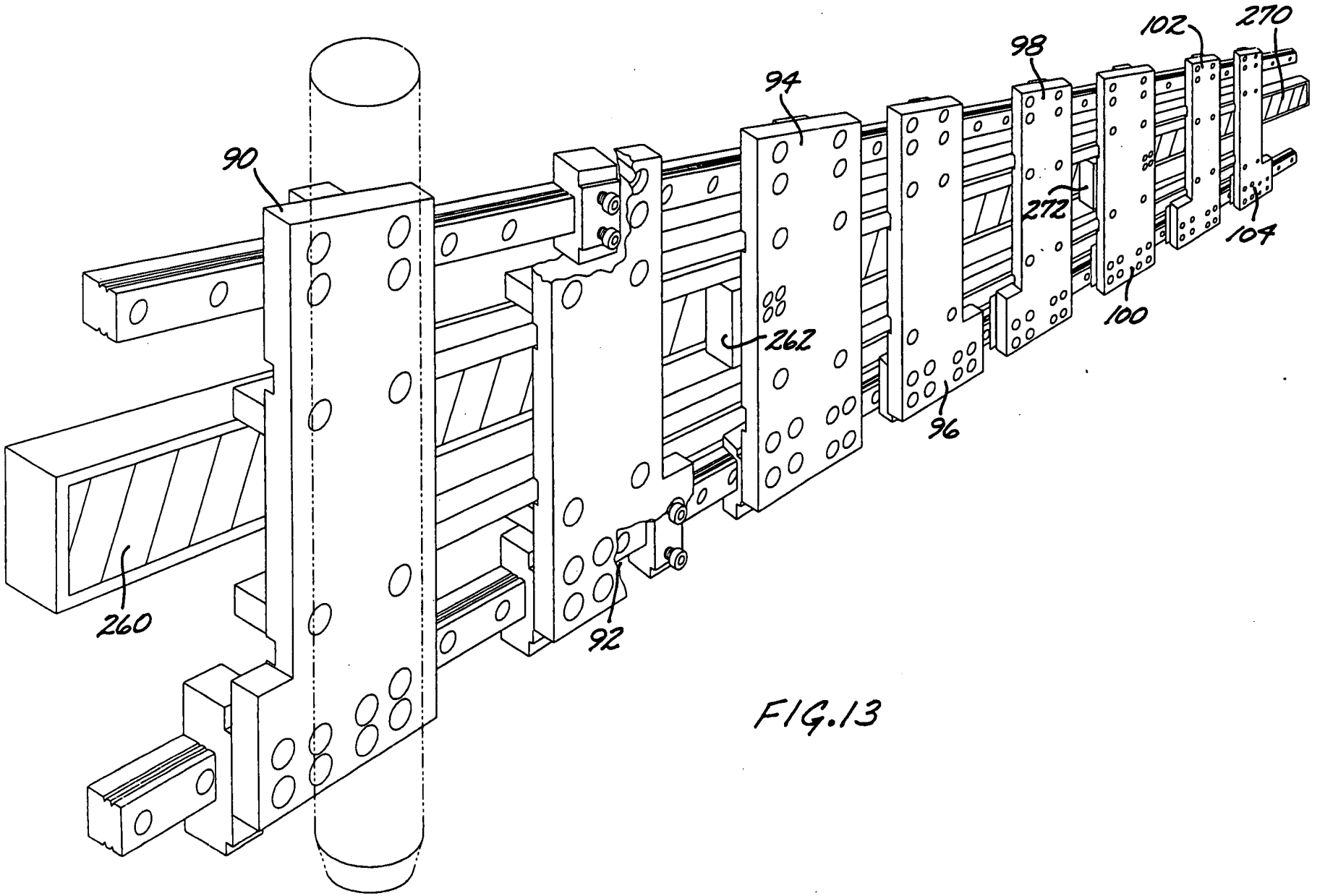


FIG.13